

CDMA Transmitter Block Diagram **1**A FIG.

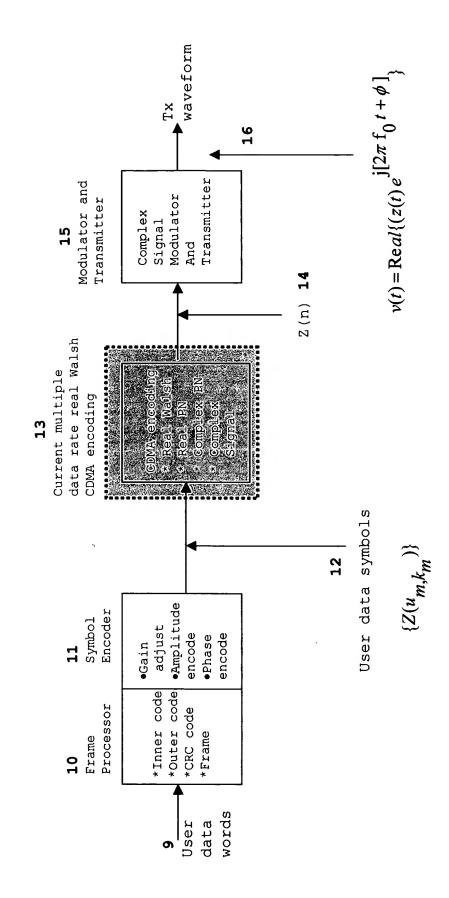




FIG. 1B CDMA Cellular Application

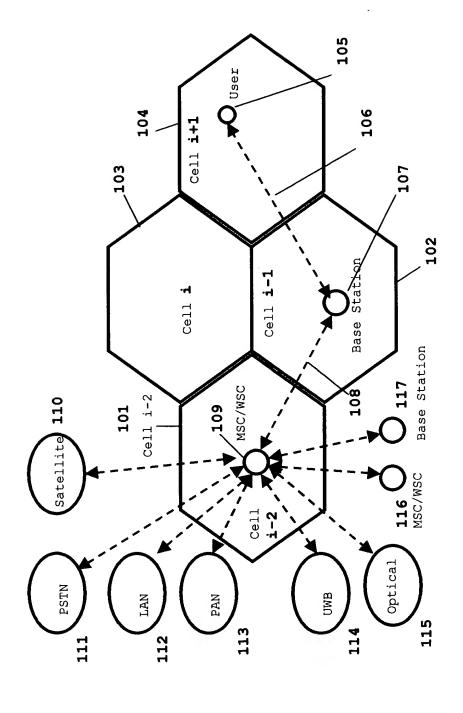




FIG. 1C Cellular Transmitter Implementation: Real Walsh

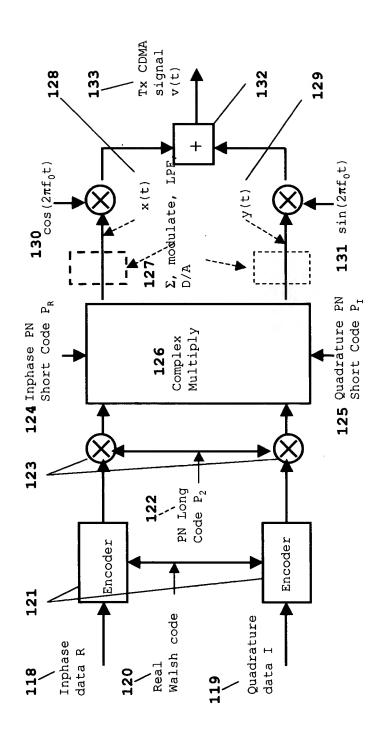


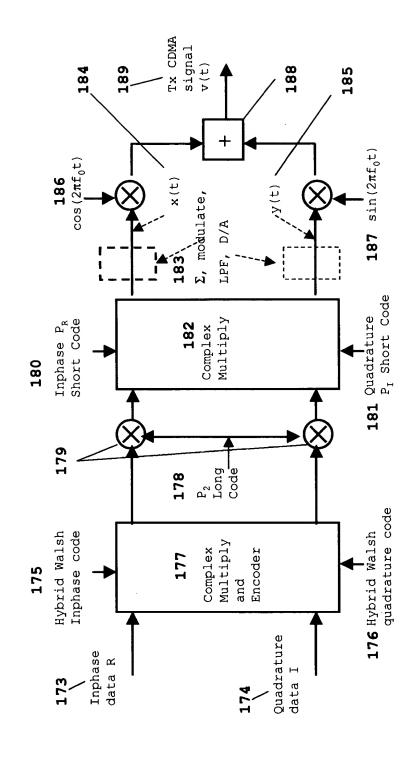


FIG. 1D Hybrid Walsh Implementation Algorithm

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	Hybrid Walsh lexicographic reordering permutation of the real Walsh code vectors	Hybrid Walsh imaginary (quadrature) code index ci	ci = 0 ci = 2c-1) ci = N-1 ci = N-2 Δ c
	Hybrid Walsh lexicogr permutation of the re	Hybrid Walsh real (inphase) code index cr	$\begin{cases} cr = 0 \\ cr = 2c \\ cr = N-1 \end{cases}$ $cr = N-1-2\Delta c$
168	Hybrid Walsh Code(Sequency) / Index c	c=0,1,,N-1	c = 0 c = 1 to (N/2-1) c = N/2 $c = N/2+\Delta c$ for $\Delta c = 1 \text{ to } N/2-1$
		167	



Cellular Transmitter Implementation: Hybrid Walsh **1**田 FIG.





Real Walsh Multiple Data Rate Encoder: **2A** FIG.

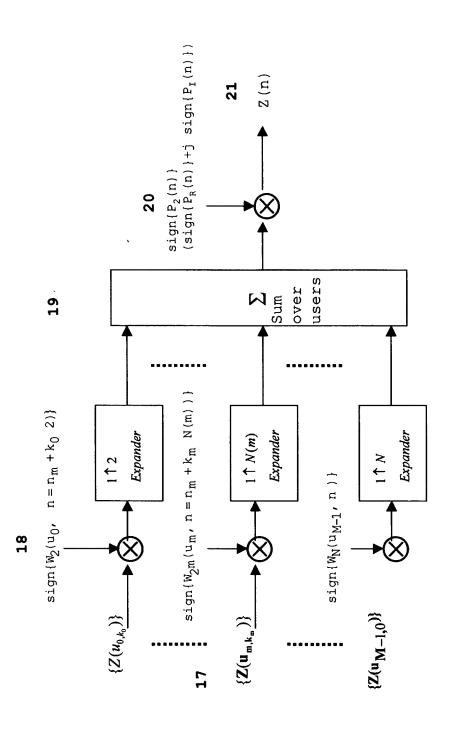
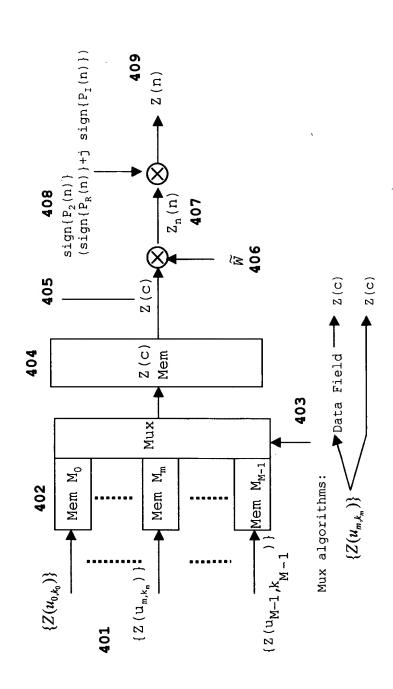




FIG. 2B Multiple Data Rate Encoder: Hybrid Walsh





CDMA Receiver Block Diagram **3A** FIG.

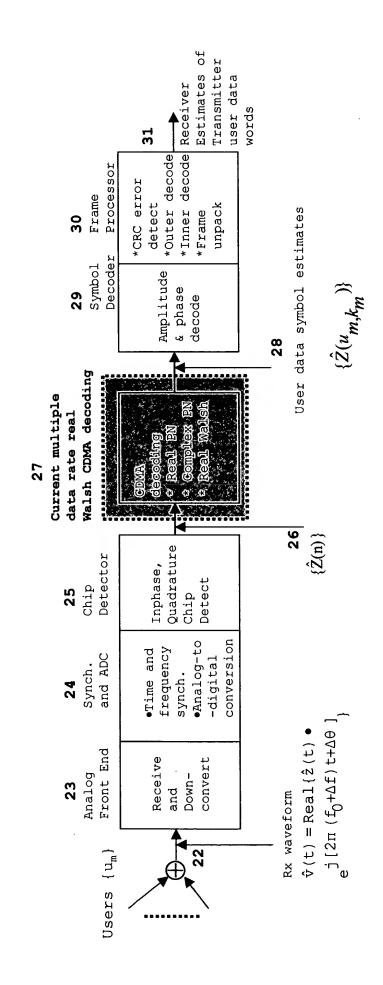




FIG. 3B Cellular Receiver Implementation: Real Walsh

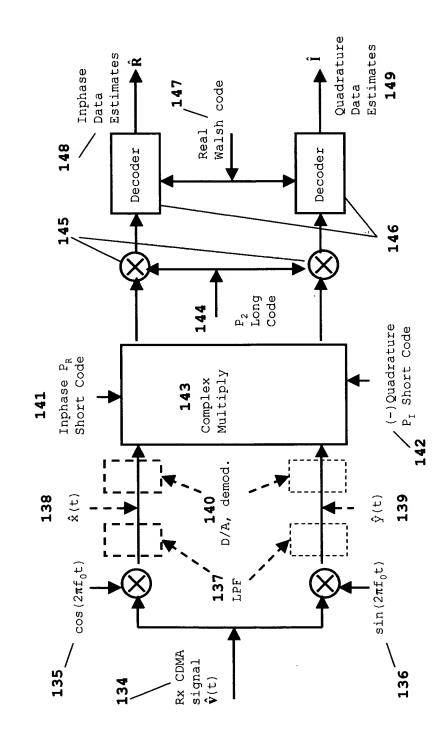




FIG. 3C Cellular Receiver Implementation: Hybrid Walsh

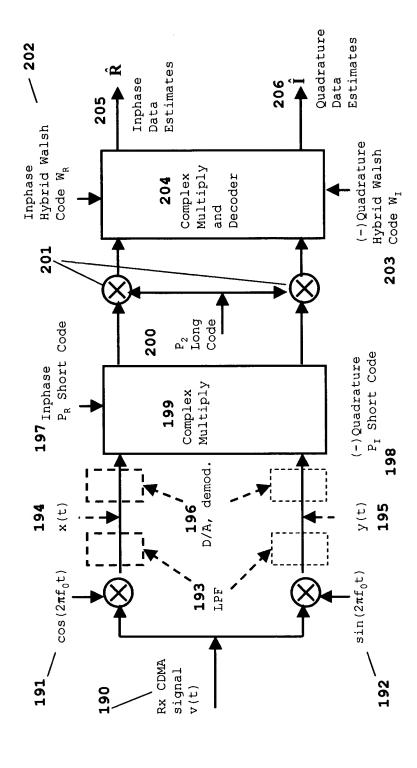




FIG. 4A Multiple Data Rate Decoding: Real Walsh

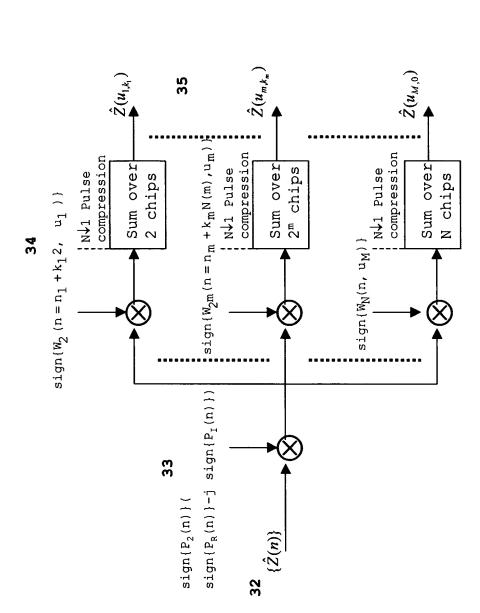




FIG. 4B Multiple Data Rate Decoding: Hybrid Walsh

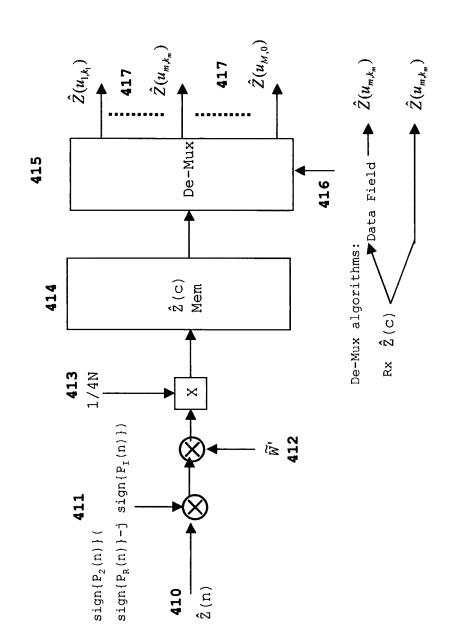
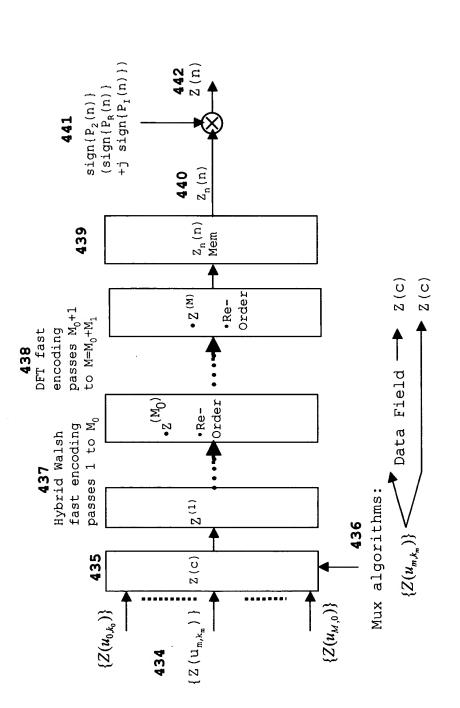


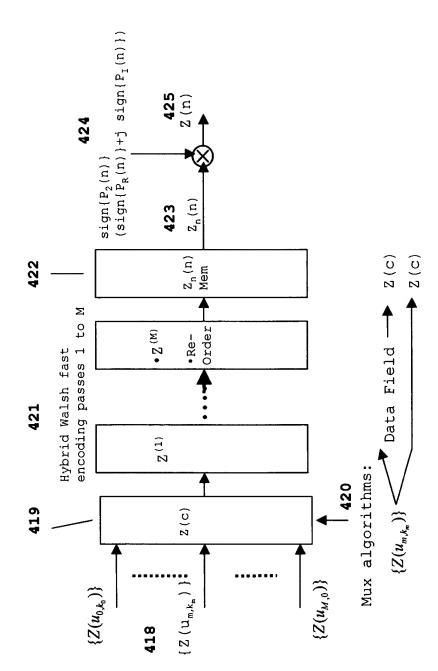


FIG. 5A Multiple Data Rate Fast Encoding for Generalized Hybrid Walsh for Example 58 in Equations (10)





Multiple Data Rate Fast Encoding for Hybrid Walsh **5B** FIG.





Multiple Data Rate Fast Decoding for Generalized Hybrid Walsh FIG. 6A

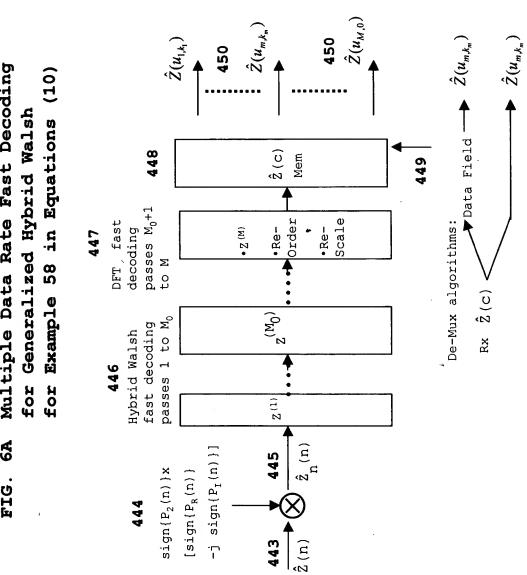




FIG. 6B Multiple Data Rate Fast Decoding for Hybrid Walsh

